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SERIAL NUMBER 10/811,835	FILING OR 371(c) DATE 03/30/2004 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 030712-29	
APPLICANTS Kazuaki Goto, Miyazaki, JAPAN; Reiko Harada, Miyazaki, JAPAN; ** CONTINUING DATA ***** ** FOREIGN APPLICATIONS ***** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/07/2004					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged Examiner's Signature _____ Initials _____		STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 3
ADDRESS 22204					
TITLE Method of designing a circuit layout of a semiconductor device					
FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		